



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Luan Tran et al. § Art Unit: 2815
Serial No.: 10/059,727 §
Filed: January 29, 2002 § Examiner: Jesse A. Fenty
For: Memory Cell Arrays § Atty. Dkt. No.: MCT.0004C1US
§ (97-0903.02/US)

Mail Stop Issue Fee

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Dear Sir:

The statement in the Reasons for Allowance attached to the Notice of Allowance incorrectly indicated that the filing date of Mori is not prior to U.S. Serial No. 08/918,657. Note that 08/918,657 was filed on August 22, 1997, whereas Mori was filed on November 8, 1996.

Respectfully submitted,

Date: Mar 17, 2006


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Date of Deposit: March 17, 2006

I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (Fax No. (571) 273-2885), on the date indicated above.

Ginger Yount

